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Patentanmeldung Nr. Patent application No. Demande de brevet n°

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Electrical device, a method for manufacturing an electrical device, test
structure, a method for manufacturing such a test structure and a method for
testing a display panel

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Electrical device, a method for manufacturing an electrical device, test structure, a method for manufacturing such a test structure and a method for testing a display panel EPO - DG 1

20 09. 2002

(72)

The invention relates to an electrical device comprising a substrate carrying at least one component comprising at least one electrode, a first connecting line for electrically connecting to said electrode, wherein said first connecting line bridges a second connecting line by means of a crossover.

US 5,936,344 discloses an electroluminescent element comprising a substrate carrying organic light-emitting devices, each having a plurality of organic material layers made of organic compounds sandwiched between an anode layer and a cathode layer. The substrate further carries a conductive connecting-line for electrically connecting an anode or a cathode to a bonding pad at the periphery of the display panel. The connecting line is made of the same material as the cathode in a standard cathode making process and has a thickness substantially the same as that of the cathode. The electroluminescent element has connecting lines at the level of the anode layer and the level of the cathode layer. A connecting line of the level of the anode layer may be interconnected with a connecting line at the level of the cathode layer by openings. By using such structures, crossovers of connecting lines can be obtained. A crossover of a connecting line is an electrically insulating structure for bridging one connecting line with another connecting line without making electrical contact between these two connecting lines. Such crossovers can be used for connecting the anode and the cathode of each electroluminescent element to the appropriate bonding pad, or for re-routing the connecting lines of an electrical device or an integrated circuit, such as a chip on glass.

A disadvantage of the prior art is that the process of manufacturing electroluminescent elements or integrated circuits are not suitable to cope with the trend towards high resolution, which results in a severe scale down of the electrode pitch. A considerable number of the high-resolution electroluminescent elements produced, had an electrical short circuit between two adjacent connecting lines, when using the standard cathode making process for simultaneously forming cathodes, connecting-lines and crossovers.

The invention aims, inter alia, to provide an electrical device which has a reduced risk of a short circuit between adjacent connecting lines and/or crossovers. Such an electrical device may be an electroluminescent display device or an integrated circuit wherein

adjacent connecting lines are involved. For an electroluminescent display device preferably the use of the standard cathode making process for simultaneously forming cathodes, connecting-lines and crossovers is permitted.

This aim is achieved by providing an electrical device characterised in that the crossover is, at least at one side, bounded by an electrically insulating structure. The structure accurately delimits the crossover and electrically separates the crossover on one side of the structure from the area on the other side of the structure. It was found that the number of high-resolution display panels according to the invention that had an electrical short circuit between two adjacent connecting lines, dramatically decreased. This result was obtained, even when the standard cathode making process for simultaneously forming cathodes, connecting-lines and crossovers was applied.

The structures can be aligned very accurately and can be used to make crossover lines close to each other. For example, on each side of a structure, a crossover can be established yielding a high density of crossovers and connecting-lines which is necessary for high resolution display panels. Moreover, the structure permits the use of the standard cathode making process for simultaneously forming cathodes, connecting-lines and crossovers.

The use of electrically insulating structures for the structuring of the cathode of an organic electroluminescent display panel is known for example from US 5,294 869, US 5,701,055, and US 5,962,970. The ramparts divide the metal layer deposited so that the metal layer on the one side of the rampart is formed electrically apart from the metal layer on the other side of the rampart. None of the publications however discloses the use of electrically insulating structures to define an area of a crossover for connecting lines.

It is further preferred that the material between the conductive connecting-line and the further conductive connecting-line, at the position of the crossover, is an electrically insulating non-electroluminescent material, preferably an electrically insulating organic material, such as a resist. SiO_x or SiN_x deposited by PECVD may be used as well. It should be noted that, in contrast with the prior art, the electrically insulating structures in accordance with the invention may be formed on top of the electrically insulating non-electroluminescent material which electrically isolates the crossover to be formed from the underlying structures, such as further connecting-lines or light emitting devices.

Preferably, the electrically insulating structures used for bounding a crossover according to the invention, have an overhanging portion projecting in a direction parallel to the surface of the substrate. More preferably, the structures according to the invention have

an inverse-tapered or T-shaped cross-section. By using such structures, bent or meandered connecting-lines can be formed since electrical separation of the electrically conducting layer on top of the structures is obtained.

Preferably, each crossover is surrounded, preferably completely, by a separate electrically insulating structure, which reduces the risk for a short circuit even further, because, in this case, there are two separate structures in-between two adjacent crossovers.

The electrical device may be an electroluminescent display device comprising several display pixels. A display pixel comprises a first electrode or set of electrodes and a second electrode or set of electrodes with an electroluminescent material layer sandwiched between these electrodes. The first or second electrodes may be connected to the first connecting line. The way of electrically connecting the first or second electrodes to their respectively connecting points, is particularly useful in organic electroluminescent display panels, because the manufacturing methods used for making such organic electroluminescent display panels is particularly suited and sometimes already adopted for making the structures and for using the standard cathode making process for forming cathodes, connecting-lines and crossovers in one step.

Alternatively the electrical device relates to an integrated circuit employed on a glass substrate comprising connecting lines for the integrated circuit.

The invention further relates to a method for manufacturing an electrical device comprising a crossover of at least a first connecting line over at least a second connecting line, at least one of said connecting lines connecting to an electrical device, comprising the steps of:

- Forming, either simultaneously or successively, said first connecting line and said second connecting line for said device on a substrate;
- depositing an insulating layer on or over said first connecting line and said second connecting line, at least at the positions where said crossover is to be formed,
- defining or creating openings in said insulating layer at positions where electrical contacts are to be provided with said first connecting line and a connection point,
- forming electrically insulating structures which, at least partially, bound the area where said crossover is to be formed,
- depositing an electrically conductive layer as to connect said first connecting line to said connection point, which connection point may be connected to another second connecting line.

Preferably the electrically insulating structure surrounds the crossover. It is then possible to apply the electrically insulating structures before the insulating layer is deposited. The insulating layer may then be deposited only in the region where the crossover is to be formed.

5 Preferably the method for manufacturing a crossover is applied in connecting display panel having a plurality of display pixels. Application for a display panel is particularly useful in organic electroluminescent display panels, because the manufacturing methods used for making such organic electroluminescent display panels is particularly suited and sometimes already adopted for making the structures and for using the standard 10 cathode making process for simultaneously forming cathodes, connecting-lines and crossovers.

15 The invention also relates to a test structure for testing a display panel comprising at least a first set of electrodes and a second set of electrodes, wherein said test structure is adapted to separately connect to said first set of electrodes and said second set of electrodes simultaneously from a single side of the display panel.

Testing of display panels before any connectors and/or driver electronics has been applied is key to increase the overall yield of successfully manufactured display device and thus to decrease the manufacturing costs.

20 The invention moreover relates to a method for manufacturing test structures and a method for testing display devices.

The embodiments of the invention will be described into more detail below with reference to the attached drawing (not to scale) of which

25 Fig. 1A, B schematically illustrate a display device, including the connections to the display panel and a display pixel;

Figs. 2A-D illustrate the process of connecting to a display panel shown in Fig. 1A, according to an embodiment of the invention;

Figs. 3A and B shows an enhanced view of a section of Fig. 2D.

30 Fig. 4 schematically illustrates the use of conventional comb structures for testing a display panel.

Figs. 5A-D show various embodiments of test structures according to an embodiment of the invention.

Next an embodiment of the invention wherein the electrical device is an electroluminescent display device will be discussed. It should be appreciated that the invention can also be applied for other electrical devices wherein components thereof are
5 connected via connecting lines, such as integrated circuits, polymer electronics, solid state lasers and for re-routing of signal lines or other lines on e.g. the electrical display device.

Figs. 1A and 1B show an electroluminescent display device 1 comprising a display panel 2 exhibiting a plurality of display pixels 3 (LEDs) and a single display pixel 3, respectively. The display pixels 3 are arranged in a matrix of rows and columns and are
10 connected mutually connected via first connecting lines 4 and second connecting lines 5. The display pixel 3 may be connected with further first connecting lines 6 as well.

Fig. 1B shows a schematic cross-section of a LED display pixel 3 having R, G and B sub-pixels. It should be appreciated that display pixels may be much more complex in reality; however the illustration in Fig. 1B is suited for the purpose of describing the
15 embodiment according to the invention.

The display pixel 3 comprises a substrate 7. Preferably, the substrate 7 is transparent with respect to the light to be emitted by the display sub-pixels R, G and B. Suitable substrate materials include synthetic resin which may or may not be flexible, quartz, ceramics and glass. Alternatively a metal substrate with a dielectric layer on top can be
20 applied for top emission displays. The total thickness of the substrate typically ranges from 50µm to 10mm, depending on the application. For large substrates, a larger substrate thickness is usually required.

A first electrode 8, commonly referred to as the anode is deposited over or on top of the substrate 7. Preferably said first electrode 8 is transparent with respect to the light to be emitted. For example, a transparent hole-injecting electrode material, such as Indium-Tin-Oxide (ITO), is used. Conductive polymers such as a polyaniline (PANI) and a poly-3,4-ethylenedioxythiophene (PEDOT) are also suitable transparent hole-injecting electrode materials. Preferably, a PANI layer has a thickness of 50 to 200 nm, and a PEDOT layer 100 to 300 nm. If an ITO hole-injecting electrode is used, the first electrode is preferably the
30 anode.

Next an insulating layer 9 is deposited on or over the first electrode 8. Insulating layer 9 is subsequently removed partially, at least at the positions where the display sub-pixels R, G and B are to be formed. It is noted that the invention is also applicable for monochrome displays. Next one or more layers 10, comprising luminescent

materials, are deposited on or over the first electrode 8, at least at the positions where the sub-pixels R, G and B are to be formed. Although any technique may be used to provide the electroluminescent material, if an electroluminescent material having a high molecular weight is used, the layer 10 comprising the electroluminescent material is preferably deposited as a liquid using techniques known in the art, such as ink-jet printing, spin-coating, dip-coating, Langmuir-Blodgett technique, spray-coating, and doctor-blade technique.

5 Alternatively, if an electroluminescent material having a low molecular weight is used, the layer 10 comprising the electroluminescent material is preferably deposited via a gaseous medium using techniques known in the art, such as vacuum deposition and chemical vapour deposition (CVD).

10 The layer 10 comprising an electroluminescent material is covered with a second electrode 11, which layer is at least deposited at the positions where a display sub-pixel R, G or B was formed. Second electrode 11 is commonly referred to as the cathode. When a hole-injecting electrode material, such as Indium-Tin-Oxide (ITO), is used for the 15 first electrode 8, the second electrode must be an electron-injecting electrode. Such an electron-injecting electrode is suitably made of a metal (alloy) having a low work function, such as Yb, Ca, Mg:Ag Li:Al, Ba or is a laminate of different layers such as Ba/Al or Ba/Ag electrode. Such a second electrode is generally deposited by means of vacuum deposition.

The light emitting structure of display pixels 3 may be packaged by applying 20 an encapsulation layer (not shown) over the entire structure or with a glass or metal lid.

The first connecting lines 4 and second connecting lines 5 are located outside the area of the display panel 2. Connecting lines 4, 5 enable signals to be applied to the displays pixels 3 to either the first electrodes 8 or the second electrodes 11. In Fig. 1A these signals may relate for a colour display device 1 to the colour signals red (R), green (G) and 25 blue (B). Conveniently the complex of a red, green and blue display sub-pixels is referred to as a display pixel 3. It is noted that the display sub-pixels R, G and B may be arranged in several configurations to form the display pixel 3, such as a rectangular or a triangular configuration. The display pixels 3 may be mutually connected to further first connecting lines 6 as well. The invention as described below may also be applied for e.g. routing these 30 lines over the display device 1, e.g. by routing the further connecting lines 6 as to cross the second connecting lines 5 and/or the first connecting lines 4.

As discussed above, in modern day display devices a trend toward high resolution exists, which results in a severe scale down of the electrode pitch, i.e. the distance between the anodes 8 and/or cathodes 11 within or between display pixels 3. As a

consequence, at least the distances between the first connecting lines 4, connecting to these electrodes of the display pixels, decreases in accordance with this trend. Therefore the probability of electrical short circuits between adjacent first connecting lines 4 increases, especially if the standard cathode making process is used. In the standard cathode making

5 process the cathodes 11, first connecting lines 4 and crossovers of the first connecting lines 4 over the second connecting lines 5 or further connecting lines 6 are made simultaneously

Focussing now on the area 12 of the display device 1 in Fig. 1A, wherein the connecting lines 4 and 5 meet, it can be observed that some of the first connecting lines 4 are connected to a connecting point 13 that may be connected to one of the second connecting

10 lines 5, providing a signal for the R sub-pixel, while others of the first connecting lines 4 have to cross the second connecting lines 5 by means of a crossover 14 in order to connect to another connecting point 13 at one of the second connecting lines 5, such as the ones providing signals for the G or B sub-pixels. It will be appreciated that said connecting points 13 not necessarily connect to a second connecting line 5 supplying signals, but may be a

15 bonding pad or any other connecting point as well.

Figs. 2A to 2D show a method according to the invention for the area 12 of Fig. 1A. Cross-sections X-X and Y-Y as indicated in the top-view, showing only the intersected areas, are displayed as well.

In Fig. 2A the first connecting lines 4 and second connecting lines 5 are deposited on the substrate 7 of the display device 1 at a thickness of e.g. 150 nm. Said first connecting-line 4 is typically made-up from a material having a low electrical resistivity, such as a metal or alloy. Preferably, the connecting lines 4 and 5 are made from the same material as the first electrode 8. In this case, the first electrode 8 and the connecting lines 4 and 5 can be formed simultaneously on the substrate 7. For example, Indium-Tin-Oxide (ITO) may be used for both the connecting lines 4 and 5 and the first electrode 8.

Alternatively, the connecting lines 4 and 5 are made from an ITO layer on top of the substrate, which ITO layer is covered by a layer of a material having a low electrical resistivity, such as a metal or alloy. The connecting lines 4 and 5 and the first electrode 8 may be formed by conventional deposition methods, such as vacuum deposition, and patterning methods, such as photolithography.

In Fig. 2B the deposition of an insulating layer 15 on or over the connecting lines 4, 5 and the substrate 7 is illustrated. The insulating layer 15, having a thickness of e.g. 2 µm for a photoresist, is at least applied where the crossovers 14 are to be formed, but may be applied in adjacent areas as well. The insulating layer 15 is preferably obtained from a

conventional photoresist and planarises the substrate 7. It is noted that the insulating layer 15 and the insulating layer 9 in the display panel 2 may be the same layer deposited in a single process step. Openings 16, i.e. via's in the insulating layer 15 to the first connecting lines 4 and second connecting lines 5, are defined or created in the insulating layer 15, e.g. by

5 photolithography or laser ablation.

In Fig. 2C electrically insulating structures 17 are applied on top of the insulating layer 15. The structure 17 accurately delimits, at least partially, the boundary where a crossover 14 is to be formed and thus, at least partially, the outline of the crossover 14. Although any patterning technique may be used to provide the structures, said structures
10 are preferably obtained by photolithographically patterning a conventional photoresist. A suitable width is 1 to 50 µm, or, preferably 10 to 20 µm, a suitable height 0,5 to 30 µm, a preferable height 2 to 6 µm. The transverse profile of the structure 17 is suitably rectangular, although a structure 17 having an overhanging portion projecting in a direction parallel to the surface of the substrate, is preferred.

15 In addition, crossovers 14 can be formed by surrounding them with structures 17 having an overhanging portion projecting in a direction parallel to the surface of the substrate. This is more clearly illustrated in Fig. 3, which is discussed below.

20 Preferably, each crossover 14 is surrounded by a separate structure 17, as illustrated in Figs. 2C and 2D. This reduces the risk for a short circuit even further, because, in this case, there are two separate structures 17 in-between two adjacent crossovers 14. In this case, the structure 17 may be formed before the connecting lines 4 and 5 are covered by the insulating layer 15. The covering can now be done by depositing insulating material onto the area surrounded by the structure, for example by ink-jet printing. Subsequently, said insulating material is removed at the positions where openings 16 are to be formed, for
25 example using laser ablation.

It is advantageous to deposit the layer 10 of the display pixel 3, comprising electroluminescent material, after the forming of the structures 17, because in this case the electroluminescent layers 10 are not exposed to the structure making process conditions, which may be hostile for the electroluminescent materials.

30 In Fig. 2D an electrically conductive layer 18 is applied on or over the structure shown in Fig. 2C. The crossovers 14 are formed here since the electrically conductive layer 18 connects the first connecting lines 4 with the second connecting lines 5, as is most clearly illustrated in cross-section Y-Y at the arrow. The electrically conductive layer is typically a material having a low electrical resistivity, such as a metal or alloy and

has a thickness preferably between 100-500µm. If the electrically conductive layer 18 is deposited by means of vacuum deposition, which is generally the case, the structures 17 or a part thereof may conveniently serve as a shadow mask for the formation of the crossovers 14. When exposed to the material flux from which the crossovers 14 are deposited in vacuum, the structures 17 prevent the deposition of electrically conductive material at the locations which are in the shadow region provided by the direction of the material flux with respect to the structures 17, thus obtaining patterned crossovers 14 or a patterned electrically conductive layer 18.

Preferably, the electrically conductive layer 18, providing the crossovers 14, is of the same material as the second electrode or cathode 11 of the display pixel 3. In this case, the second electrode 11 and the electrically conductive layer 18 can be formed simultaneously in one vacuum deposition step.

It was previously noted with respect to the Fig. 2A-D that the drawings were not to scale. In Figs. 3A and B a more realistic impression is given of the dimensions of the various layers and the shapes of the layers in the X-X cross-section, indicated by 19 in Fig. 2D and the Y-Y cross-section, indicated by 20 in Fig. 2D respectively.

The display panel 2 including the connecting lines may be packaged by using an encapsulation layer (not shown) e.g. if the crossovers 14 are to be permanently applied in the electrical device 1. Alternatively a thin film package, comprising organic or anorganic components or combinations thereof, can be applied for protection. Such a thin film package may substantially reduce diffusion of water into the structure. If the crossover structure is applied for testing, as will be discussed below, the crossovers 14 do not have to be packaged by the encapsulation layer but may be carefully handled until the test has been performed.

Various aspects of the method of manufacturing crossovers as described above can be used advantageously for testing an electrical device such as the display panel 2. Testing of display panels 2 before any connectors and/or driver electronics has been applied is key to increase the overall yield of successfully manufactured display device and thus to decrease the manufacturing costs.

Conventionally two methods can be distinguished in which such the appearance of these shortcuts can be tested. First is contacting all the electrodes 8, 11 of the display panel 2 separately. This type of testing involves high expenses, since machine positioning with an accuracy of the electrode pitch is needed, and is therefore economically unattractive. Secondly, test comb structures 21, 22 can be applied, as shown in Fig. 4. In using this comb structures, each second anode 8 is connected on one side of the display panel

2 with a first comb structure 21 and the other remaining anodes 8 are connected to a second comb structure 21' at the other side of the display panel 2. The same procedure is applied for the cathodes 11.

Three types of short cuts can be observed in display panels 2. Firstly, defects
5 in the electroluminescent layer 10 may result in short cuts between the first electrode or anode 8 and the second electrode or cathode 11 (AC-shorts). Secondly, adjacent anodes 8 in the display panel matrix 2 may connect, i.e. a A-A short exists. Thirdly, adjacent cathodes 11 may form a C-C shortcut. By measuring the resistances using the comb structures 21, 22 the presence of shortcuts A-A, C-C and A-C can be detected. The comb-structures 21, 22 are
10 removed after testing.

Using these test comb structures is a low cost alternative, but using these comb structures for modern day display panels, being colour displays and split or tiled displays and displays with a very small electrode pitch, is difficult, if not impossible. For split displays only one side of the display is available for connection by a test comb structure. The R, G and B
15 sub-pixels have appeared to have different failure characteristics, i.e. shortcut behaviour between the R, G and B sub-pixels is different. Therefore preferably each colour R, G and B is to be tested separately as a result of which three comb structures are needed. Unfortunately each side of the display panel two can only accommodate one comb structure.

In Fig. 5A-D various embodiments of test structures are displayed wherein
20 various aspects of the manufacturing of crossovers according to the invention have been applied. In general the method of defining crossovers according to the invention allows one single side of the display panel 2 to accommodate multiple comb structures 21, 22.

In Figs. 5A and B two embodiments of comb structures for testing a monochrome split display panel 2 are displayed. In both embodiments, a test structure 23
25 comprises two comb structures, applied on one side of the display panel 2.

In Fig. 5A first connecting lines 4 and 4' and second connecting lines 5 and 5' are applied on the substrate. Next the insulating layer 15 is deposited on or over the connecting lines 4, 4', 5 and 5'. Openings 16 are created or defined either before or after deposition of the insulating layer 15 as to enable connection of the first connecting lines 4' to the second connecting lines 5' afterwards. To achieve this connection the electrically conductive layer 18 is deposited in the area indicated by 24. The test structure 23 enables the electrodes 8 and 8', each belonging to a different display panel 2 of the split display, to be approached and connected to simultaneously from one side of the display panel.

Alternatively, the second connecting lines 5 and 5' are applied in a different stage of the process and the insulating layer 15 is deposited accordingly, as shown in Fig. 5B.

For colour display panels 2, a test structure 25 comprising three comb structures per panel side may be required, as explained above. In Figs. 5C and 5D, two 5 embodiments of such a test structure 25 are displayed.

The display panel 2 comprises three alternating sets of electrodes 11, 11' and 11''. First connecting lines 4, 4' and 4'' are applied for connecting to said sets of electrodes as well as a second connecting line 5. Next, the insulating layer 15 is deposited on or over the connecting lines 4, 4', 4'' and 5 and openings 16 are created or defined in order to enable 10 connection to the first connecting lines 4' en 4'' afterwards. Finally the electrically conductive layer 18 is applied in the areas indicated by 26, which may be separated by applying a shadow mask.

In Fig. 5D a test structure 25 is shown wherein the electrically insulating structures 17 are used, in order to separate the connections to be made to the first connecting 15 lines 4' and 4'' respectively if the electrically conducting layer 18 is applied. Such a test structure 25 can be made very compact as the openings 16 can be created more closely to each other by using the structures 17, in contrast to the process using shadow masks as displayed in Fig. 5C. The structures 17 can be applied in various stages of the process described for the test structure of Fig. 5C. Preferably the structures 17 are however applied 20 after deposition of the insulating layer 15.

In summary the invention relates to the use of electrically insulating structures to define crossovers of connecting lines, which are to be bridged. At the crossover the connecting lines are covered with a insulating layer in which openings or contact holes are formed. The electrically insulating structures are preferably be deposited on or over this 25 insulating layer. Preferably an electrically insulating structure is used for each crossover as to minimise the risk of short circuit between adjacent connecting lines. The electrically insulating structures can be aligned very accurately and in using these structures, the standard cathode making process can be applied to make small connecting lines close to each other. The invention allows new testing and routing methods, and efficient lead-outs for electrical 30 devices. Such an electrical device may be an organic light emitting diode or a poly-LED.

For the purpose of teaching the invention, preferred embodiments of the electrical device, the method of manufacturing a crossover, the test structure and the method of testing have been described above. It will be apparent for the person skilled in the art that

other alternative and equivalent embodiments of the invention can be conceived and reduced to practice without departing from the true spirit of the invention.

CLAIMS:

EPO - DG 1

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1. An electrical device comprising a substrate carrying at least one component comprising at least one electrode, a first connecting line electrically connected to said electrode, wherein said first connecting line bridges a second connecting line by means of a crossover

5 characterised in that

the crossover is, at least at one side, bounded by an electrically insulating structure.

10 2. An electrical device according to claim 1, wherein said electrically insulating structure extends into a direction substantially perpendicular to said substrate and comprises at least one overhanging portion projecting in a direction substantially parallel to the surface of said substrate.

15 3. An electrical device according to claim 1, wherein said crossover is, preferably completely, surrounded by said electrically insulating structure.

4. An electrical device according to claim 1, wherein said electrical device comprises several first connecting lines, at least some of said first connecting lines having a crossover with at least said second connecting line, wherein each crossover is bounded by a an electrically insulating structure .

20 5. An electrical device according to claim 1, wherein said electrical device is an electroluminescent display device and said component is a display pixel.

25 6. An electrical device according to claim 5, wherein said display pixel comprises a first electrode, an electroluminescent material and a second electrode, said first or second electrode being connected to said first connecting line.

7. An electrical device according to claim 1, wherein said electrical device is an integrated circuit.

8. An electrical device according to claim 7, wherein said substrate is made of glass.

5 9. A method for manufacturing an electrical device comprising a crossover of at least a first connecting line over at least a second connecting line, at least one of said connecting lines connecting to an electrical device, comprising the steps of:

- forming, either simultaneously or successively, said first connecting line and said second connecting line for said device on a substrate;
- 10 - depositing an insulating layer on or over said first connecting line and said second connecting line, at least at the positions where said crossover is to be formed,
- defining or creating openings in said insulating layer at positions where electrical contacts are to be provided with said first connecting line and a connection point,
- forming electrically insulating structures which, at least partially, bound the area where said crossover is to be formed,
- 15 - depositing an electrically conductive layer as to connect said first connecting line to said connecting point, which connecting point may be connected to another second connecting line.

20. 10. A method according to claim 9, wherein said electrically insulating structure is formed so as to extend in a direction substantially perpendicular to said substrate and to comprise at least one overhanging portion projecting in a direction substantially parallel to the surface of said substrate.

25 11. A method according to claim 9, wherein said electrically insulating structure surrounds the crossover.

12. 11. A method according to claim 9, wherein said electrical device is an electroluminescent display device having at least one display pixel, comprising a first electrode, an electroluminescent material and a second electrode, further comprising the steps of:

- forming said first electrode simultaneously with said first connecting line and/or said second connecting line,

- forming an electroluminescent layer on or over said first electrode, at least at the positions where display pixels are to be formed,
- forming said second electrode simultaneously with said electrically conductive layer, at least at the positions where said display pixel is to be formed, as to connect said first or second electrode with said first connecting line.

13. A method according to claim 12, wherein said electroluminescent layer is formed after said formation of said electrically insulating structure.

14. A method according to claim 9, wherein said electrical device is an integrated circuit and said first connecting line is connected to said integrated circuit.

15. A method according to claim 14, wherein said integrated circuit is made on a glass substrate.

15

16. Test structure for testing a display panel comprising at least a first set of electrodes and a second set of electrodes, wherein said test structure is adapted to separately connect to said first set of electrodes and said second set of electrodes simultaneously from a single side of the display panel.

20

17. Test structure according to claim 16, wherein said test structure comprises multiple comb structures each comprising multiple first connecting lines and a second connecting line wherein crossovers for said first connecting lines and second connecting lines are used to enable connection of said test structure to said single side of the display panel.

25

18. Test structure according to claim 17, wherein said crossover is formed by an electrically conductive layer deposited on or over an insulating layer having openings to said first or second connecting lines.

30

19. Test structure according to claim 18, wherein electrically insulating structures separate at least some of said openings.

20. Method for manufacturing a test structure according to any of the claims 16-19 using one or more steps of the method of manufacturing a crossover according to any of the claims 9-11.

5 21. Method for testing a display panel or display pixel wherein a test structure according to any of the claims 16-19 is used.

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EPO - DG 1

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ABSTRACT:

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The invention relates to an electrical device comprising a substrate carrying at least one component comprising at least one electrode, a first connecting line electrically connected to said electrode, wherein the first connecting line bridges a second connecting line by means of a crossover. The crossover is, at least at one side, bounded by an electrically insulating structure. The invention allows new testing methods and efficient lead-outs for an electrical device, such as electroluminescent display devices.

5
Fig. 1A

(72)

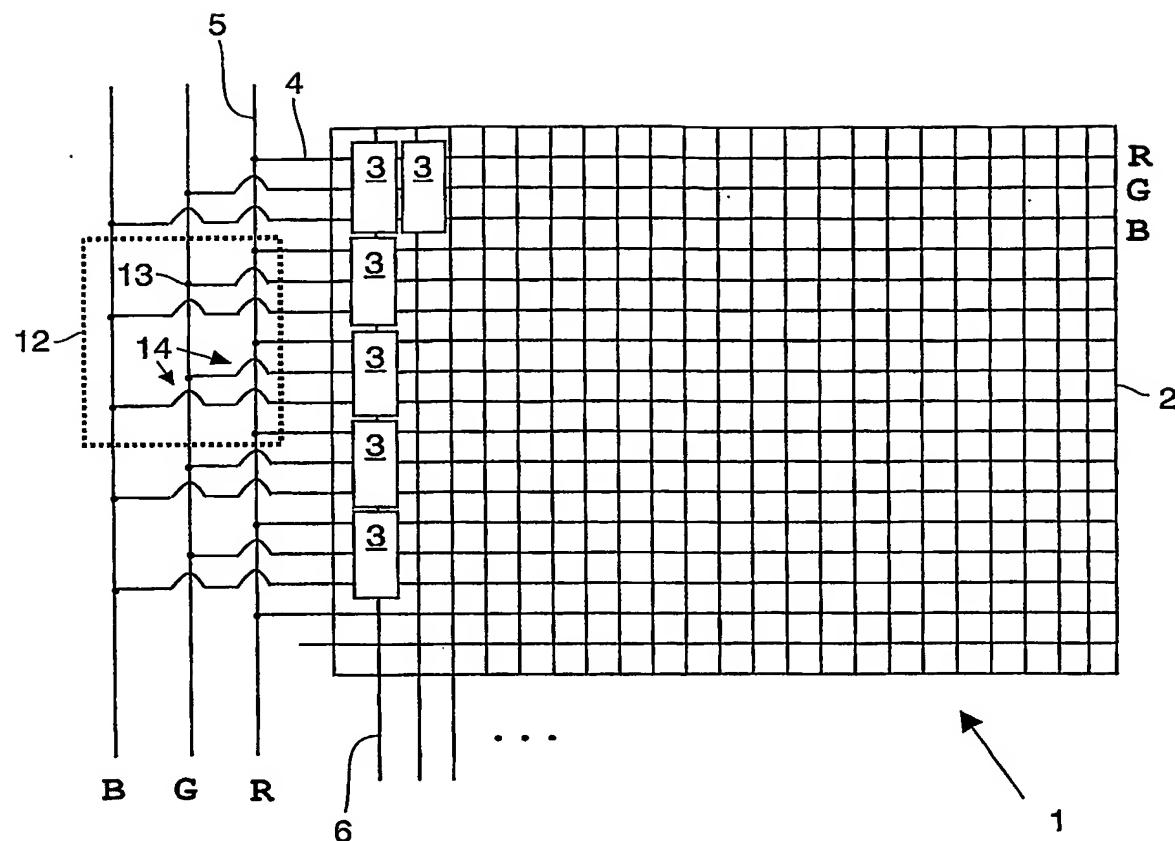


FIG. 1A

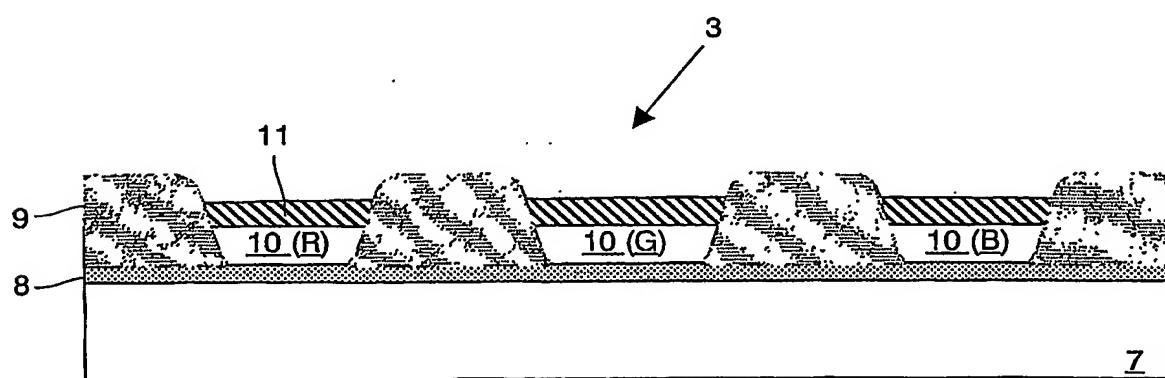


FIG. 1B

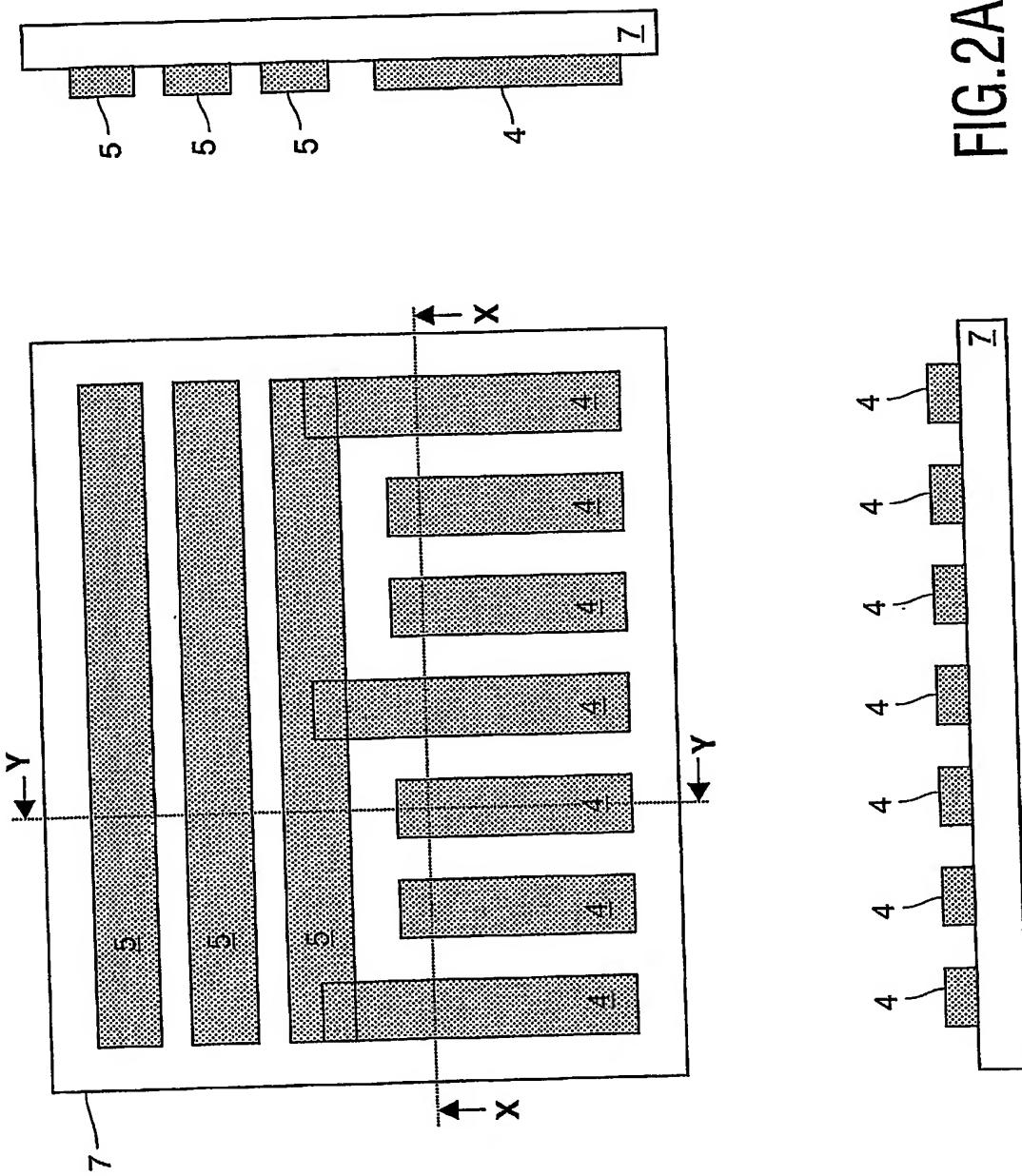
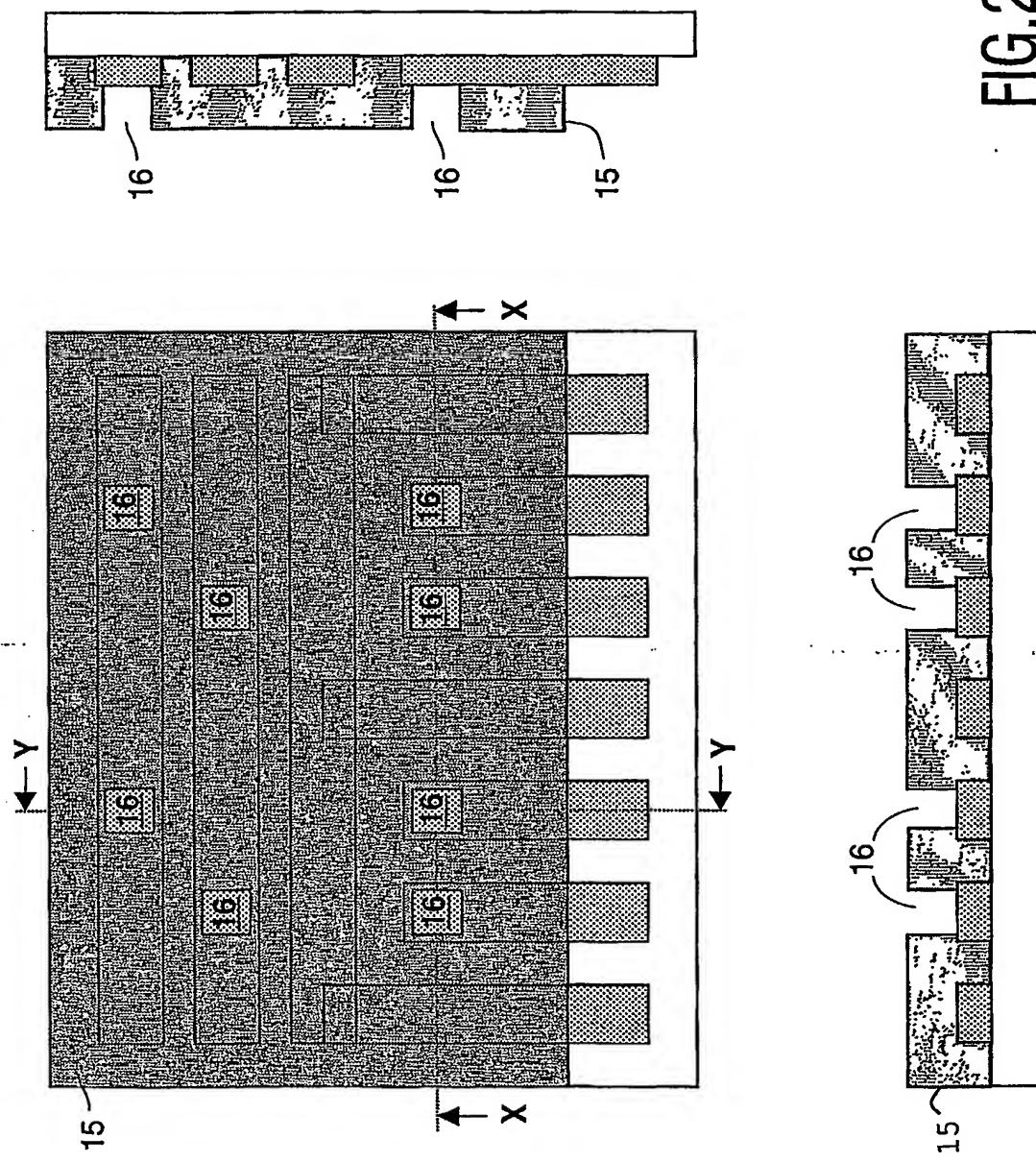
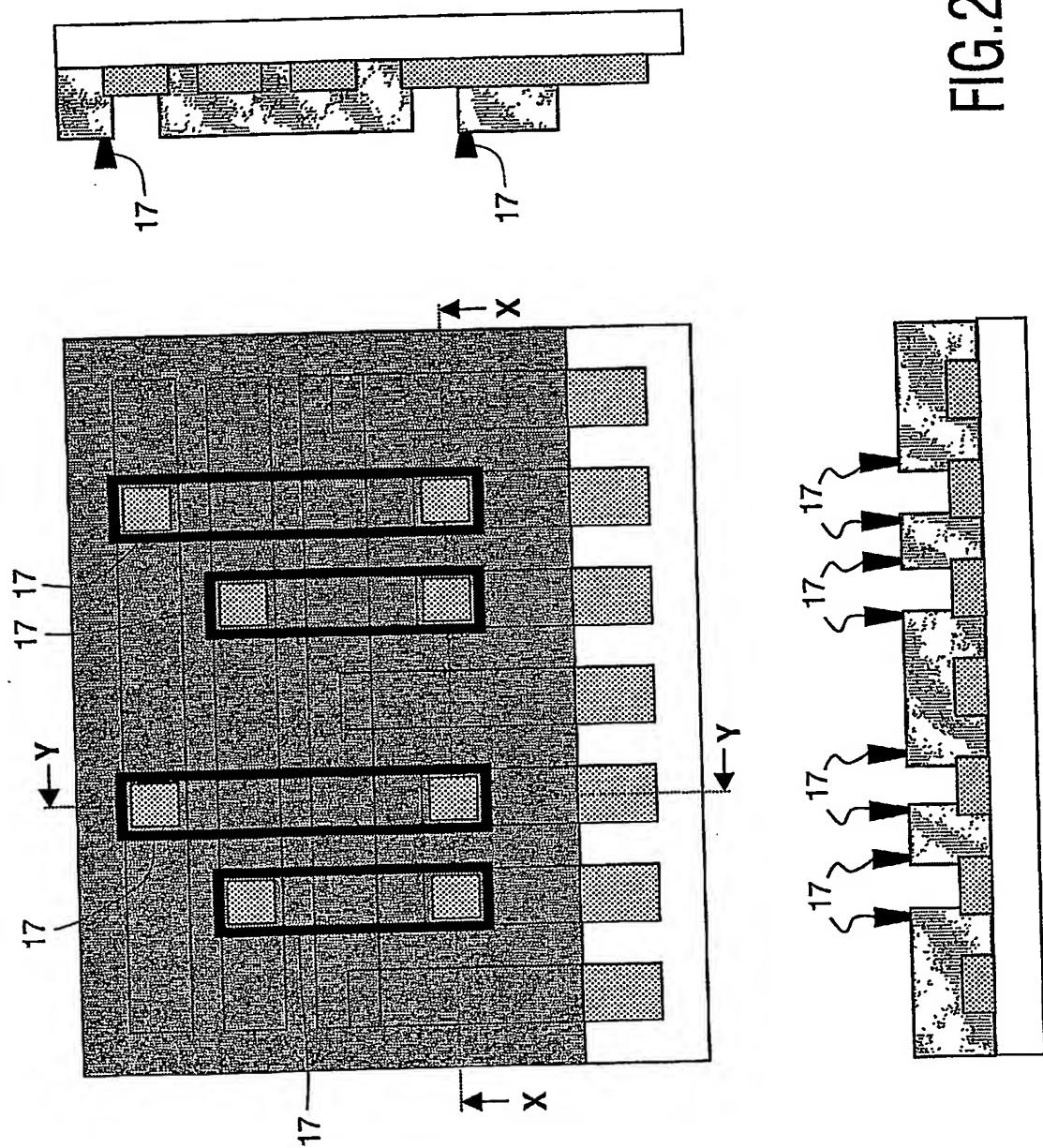
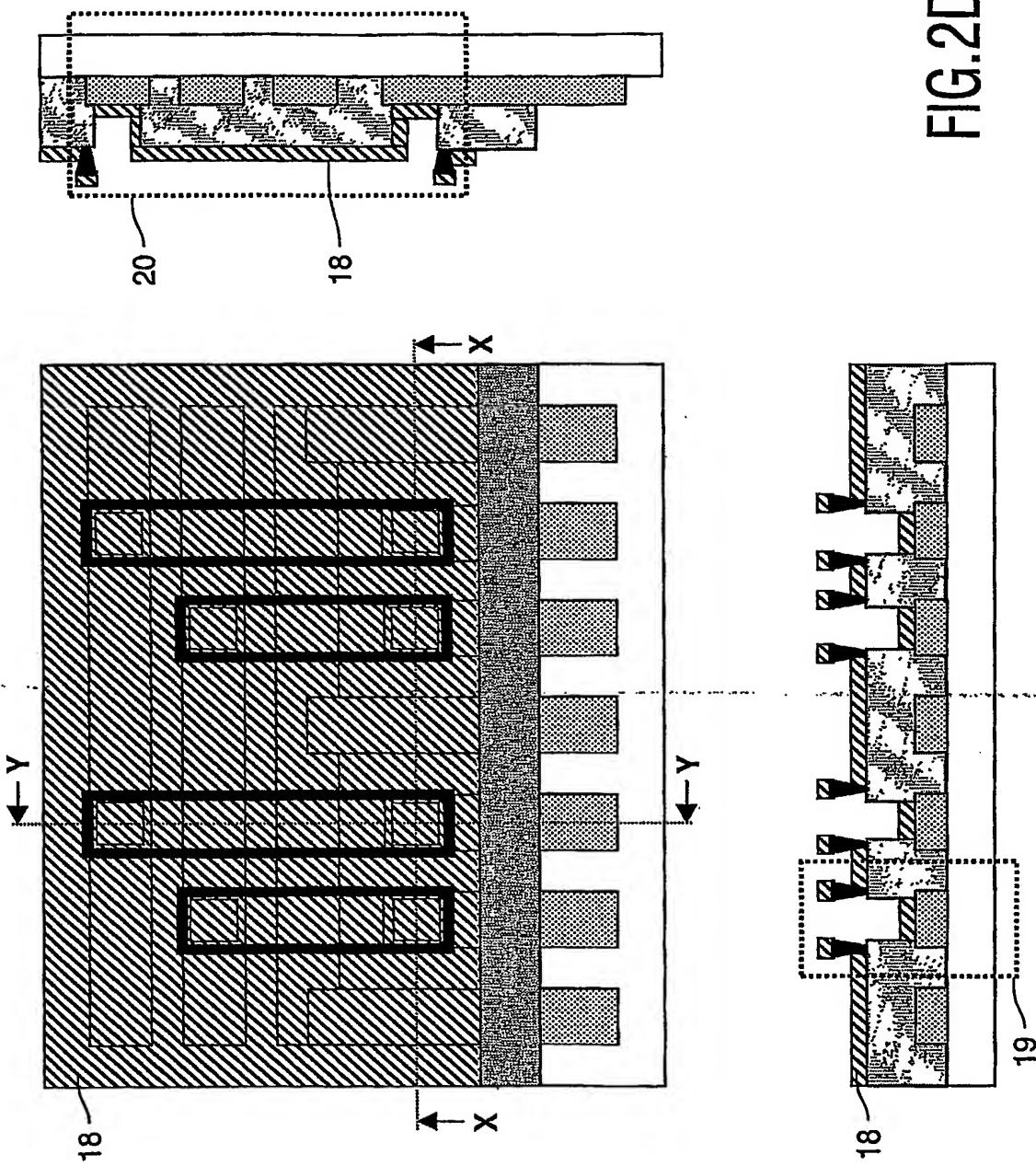


FIG. 2A







6/8

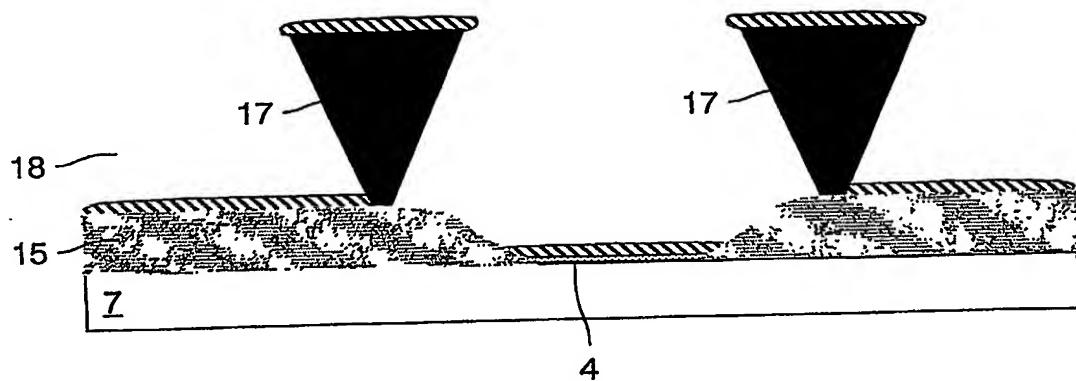


FIG.3A

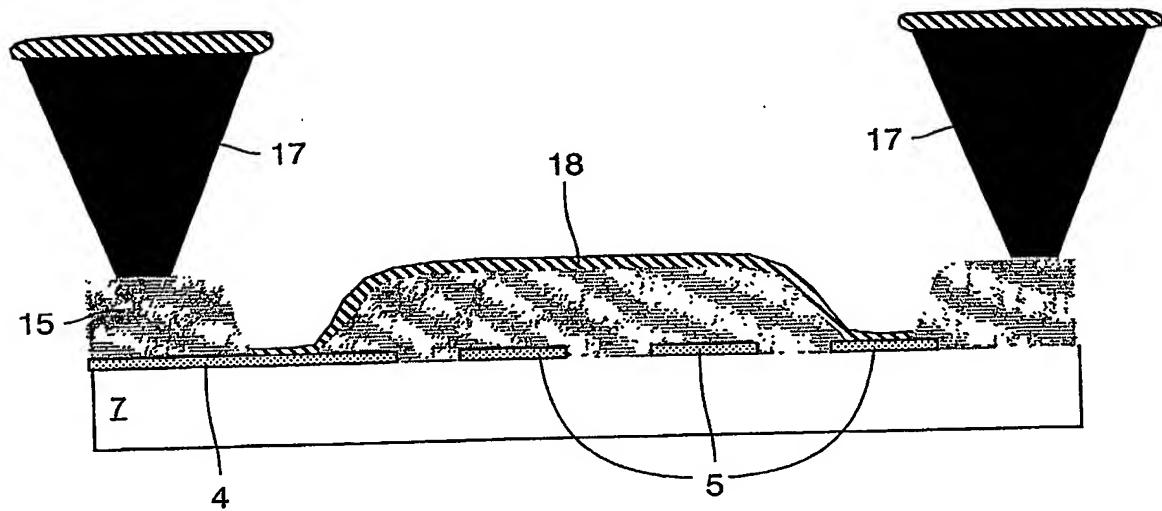


FIG.3B

7/8

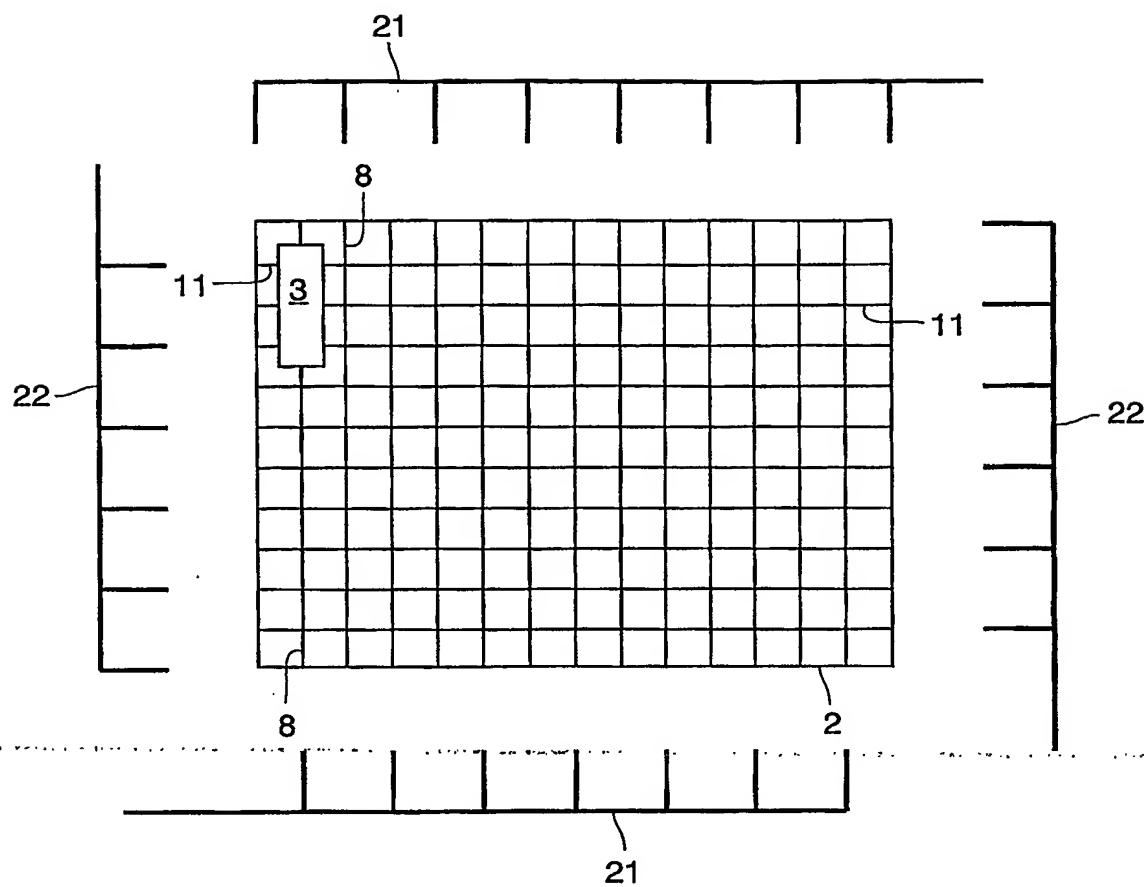


FIG.4

8/8

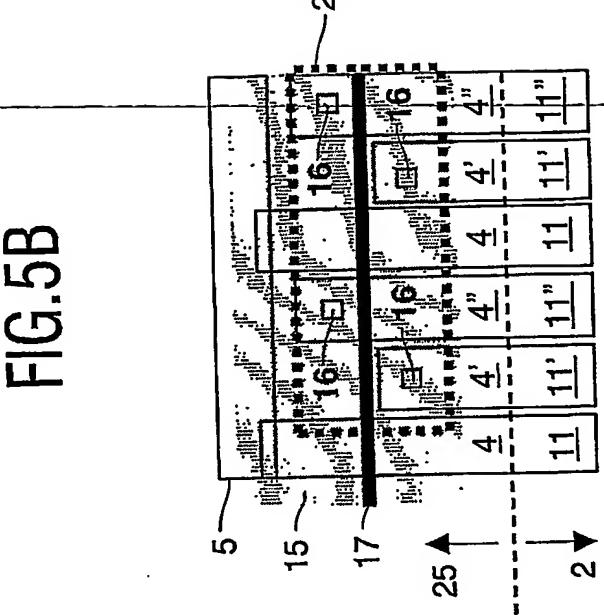
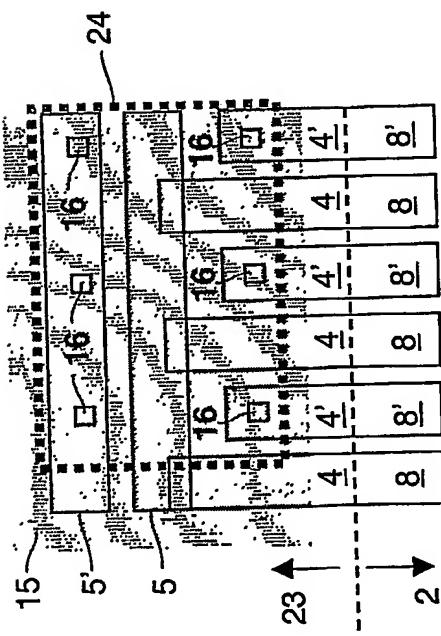


FIG. 5B

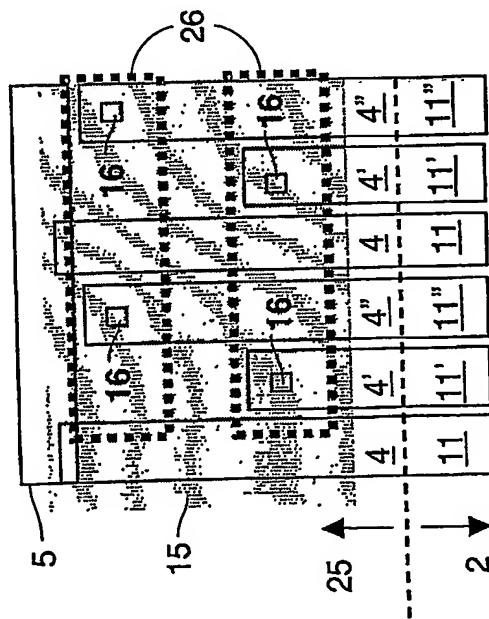


FIG. 5D

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